

1 IN THE CLAIMS

2

3 Please amend claims 1-7 and 22-23 as follows:

4

5 1. (Currently Amended) Each of a plurality of coupling circuits A-coupling
6 circuit for a Serial ATA storage device, comprising:

7 a first Serial ATA controller-side transceiver receiving a first Serial ATA
8 communication path;

9 a second Serial ATA controller-side transceiver receiving a second Serial ATA
10 communication path;

11 a Serial ATA storage device-side transceiver;

12 coupling circuit switches selectively coupling either the first Serial ATA controller-
13 side transceiver or the second Serial ATA controller-side transceiver to the Serial ATA
14 storage device-side transceiver; and

15 a microcontroller adapted to control the coupling circuit switches.

16 2. (Currently Amended) Each of the plurality of coupling circuits The coupling
17 circuit of claim 1, further comprising an out of band squelch control component for
18 activating the first Serial ATA controller-side transceiver receiving a first Serial ATA
19 communication path, the second Serial ATA controller-side transceiver receiving a
20 second Serial ATA communication path, and the Serial ATA storage device-side
21 transceiver.

22

23 3. (Currently Amended) Each of the plurality of coupling circuits The coupling
24 circuit of claim 1, wherein the microcontroller includes a processor coupled to a power
25 switch and coupled to the coupling circuit switches.

26

27 4. (Currently Amended) Each of the plurality of coupling circuits The coupling
28 circuit of claim 1, wherein the microcontroller includes a processor coupled to a set of D
29 flip-flops that are coupled to a power switch and coupled to the coupling circuit switches.

30

1 5. (Currently Amended) Each of the plurality of coupling circuits ~~The coupling~~
2 circuit of claim 1, wherein the microcontroller is programmed to as follows:
3 switch the coupling circuit to a first storage controller;
4 switch the coupling circuit to a second storage controller;
5 power up the Serial ATA storage device; and
6 power down the Serial ATA storage device.

7
8 6. (Currently Amended) Each of the plurality of coupling circuits ~~The coupling~~
9 circuit of claim 5, wherein the microcontroller is further programmed to as follows:
10 write data to a memory;
11 read data from the memory; and
12 read the status of the coupling circuit.

13
14 7. (Currently Amended) Each of the plurality of coupling circuits ~~The coupling~~
15 circuit of claim 6, wherein the status includes information on whether the Serial ATA
16 storage device is coupled to the first Serial ATA controller-side transceiver or the
17 second Serial ATA controller-side transceiver, the Serial ATA storage device is powered
18 up or down, the communication status, and/or the board revision and code revision
19 levels of the coupling circuit.

20
21 22. (Currently Amended) Each of a plurality of coupling circuits ~~A coupling~~
22 circuit for a storage device, comprising:
23 a first controller-side transceiver receiving a first communication path;
24 a second controller-side transceiver receiving a second communication path;
25 a storage device-side transceiver;
26 coupling circuit switches selectively coupling either the first controller-side
27 transceiver or the second controller-side transceiver to the storage device-side
28 transceiver; and
29 a microcontroller adapted to control the coupling circuit switches and control the
30 power to the storage device.

1 23. (Currently Amended) Each of a plurality of coupling circuits A-coupling

2 circuit for a Serial ATA storage device, comprising:

3 means for receiving a first Serial ATA communication path;

4 means for receiving a second Serial ATA communication path;

5 means for coupling either the first Serial ATA communication path or the second

6 Serial ATA communication path to the Serial ATA storage device; and

7 a microcontroller adapted to control the coupling circuit switches.

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

24

25

26

27

28

29

30